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		FLOW 1	FLOW 2	FLOW 3	TC
	CC1	128	32	0	160
	CC2	64	32	8	96
ALLOWED SUBSET OF CCs	(CC3	128	64	8	200
	CC4	64	0	0	64
	CC5	0	0	8	8

CAPACITY e.q. BIT RATE

FIG. 1

		FLOW 1	FLOW 2	FLOW 3	TC	
	CC1	128	64	8	200	
	CC2	128	32	0	160	
ALLOWED SUBSET <	(CC3	64	32	8	96	POINTER
	CC4	64	0	0	64	TOMTER
	CC5	0	0	8	8	

FIG. 2

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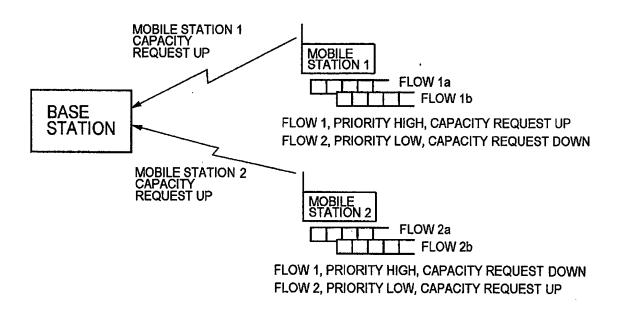
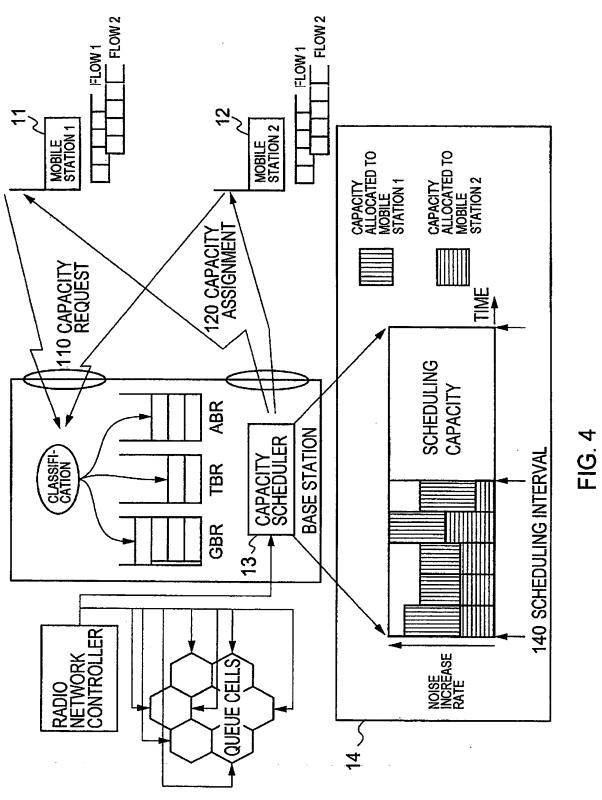


FIG. 3

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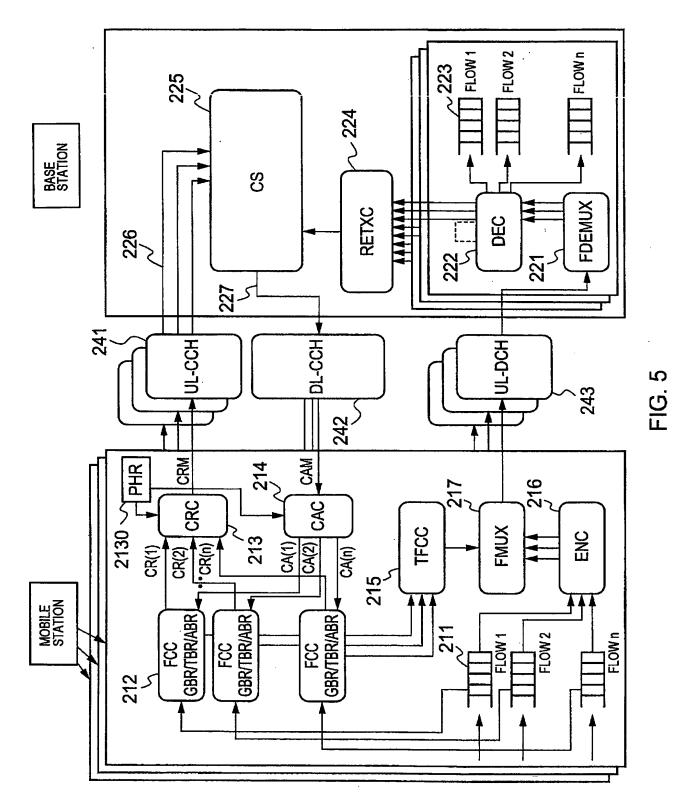


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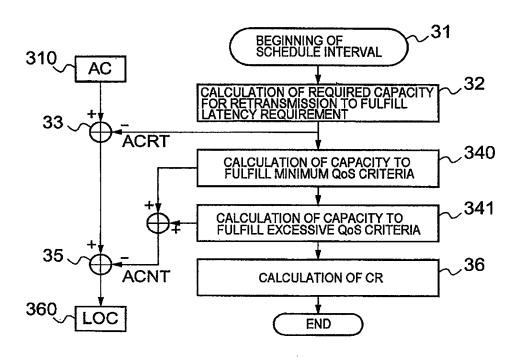


FIG. 6

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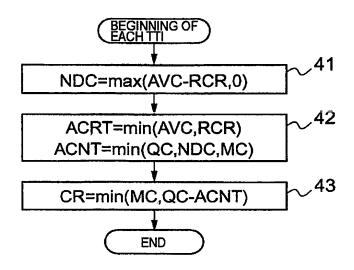


FIG. 7

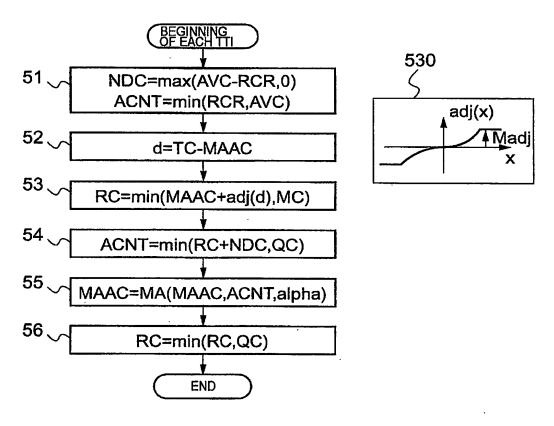


FIG. 8

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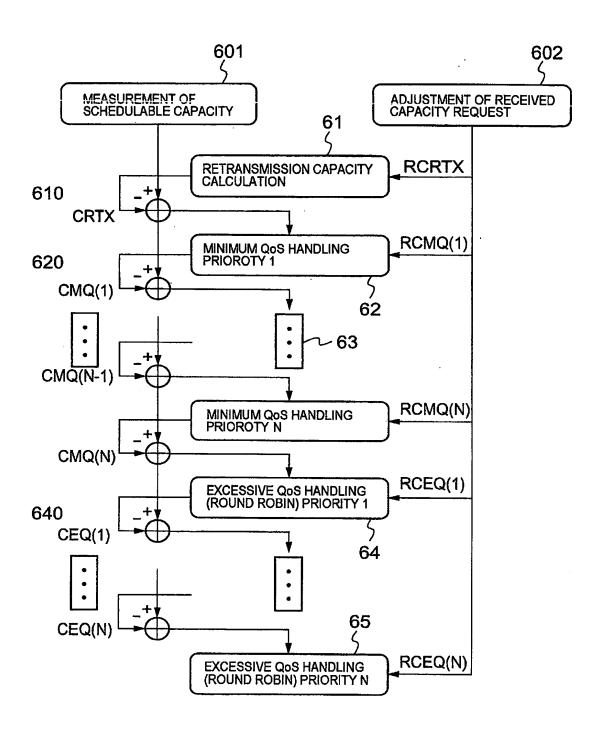


FIG. 9

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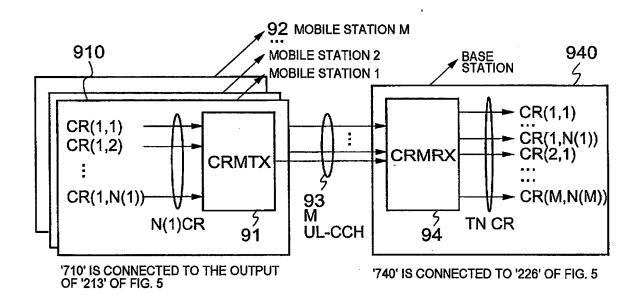


FIG. 10

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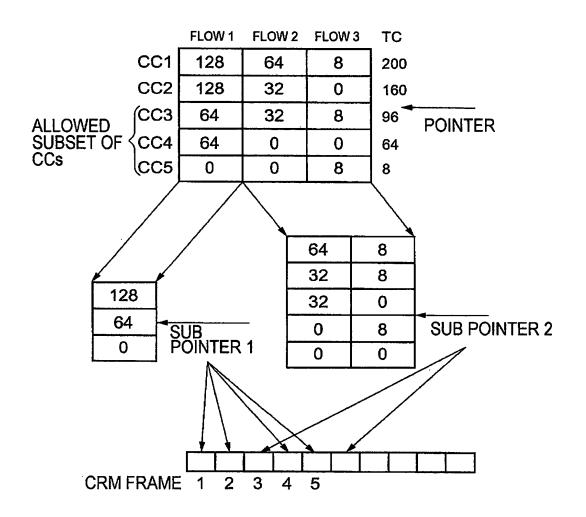


FIG. 11

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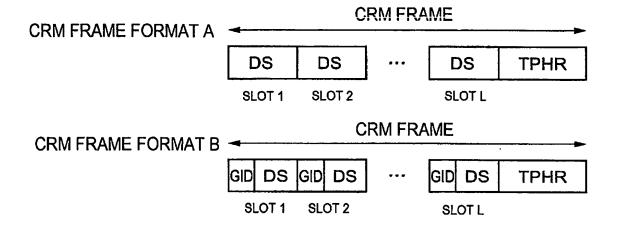


FIG. 12

									_ c1
FRAME NUMBER	0	1	2	3	4	5	6	7	\sim
SLOT = 1	1	2	1	2	1	2	1	2	1
SLOT = 2	3	3	3	4	3	3	3	4]
				-					. c2
FRAME NUMBER	0	1	2	3	4	5	6	7	hy 2
SLOT = 1	1	1	1	4	3	2	1	3	
SLOT = 2	2	2	2	2	2	3	3	4	1

FIG. 13

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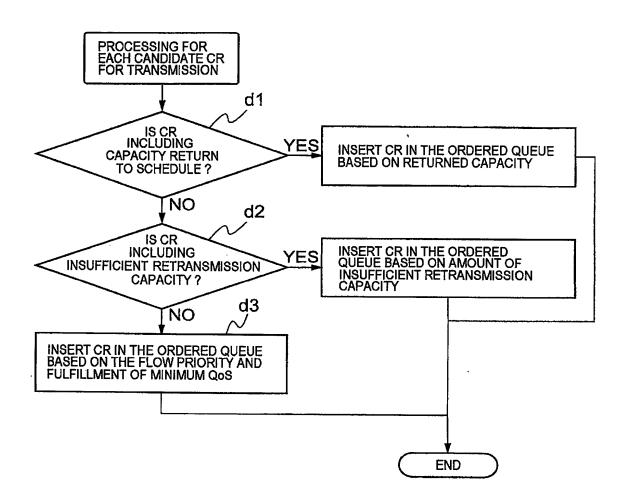
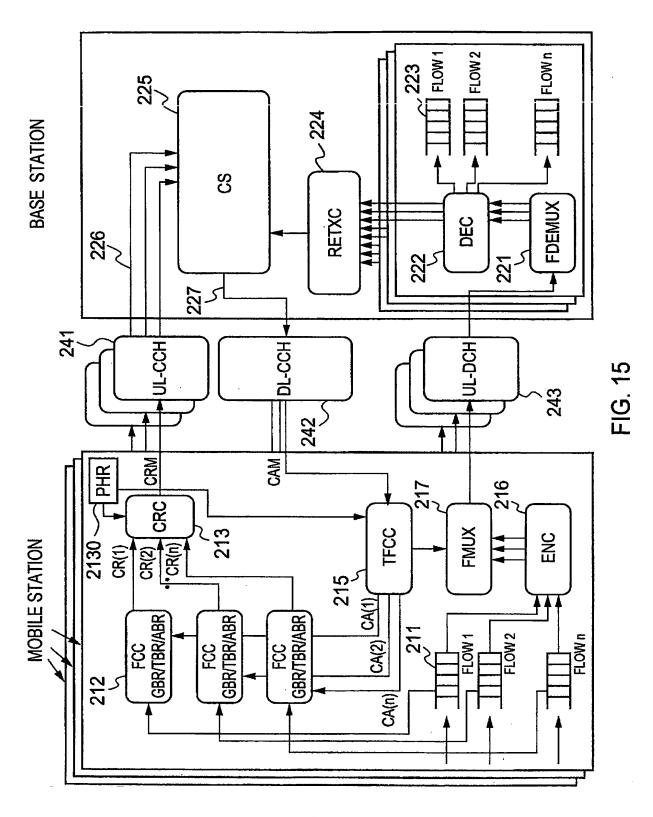


FIG. 14



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